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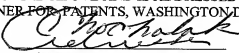
12/27/01

1c973 U.S. PTO

PATENT APPLICATION
Attorney's Do. No. 9903-031

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

10/27/01
9903/01
U.S. PTO

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<p>I HEREBY CERTIFY THAT THIS PAPER AND ENCLOSURES AND/OR FEE ARE BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE "EXPRESS MAIL POST OFFICE TO ADDRESSEE" SERVICE UNDER 37 CFR 1.10 ON THE DATE INDICATED ABOVE AND IS ADDRESSED TO: BOX PATENT APPLICATION, ASSISTANT COMMISSIONER FOR PATENTS, WASHINGTON, D.C. 20231.</p>	
<u>ADRIENNE CHOCHOLAK</u> (SENDER'S PRINTED NAME)	 (SIGNATURE)

Box Patent Application
Assistant Commissioner for Patents
Washington, D.C. 20231

Enclosed for filing is a patent application under 37 CFR 1.53(b) of:

Inventor Tae-Sub CHANG and Dong-Ho LEE:
For: HIGH FREQUENCY SEMICONDUCTOR CHIP PACKAGE AND
 SUBSTRATE

Applicant requests FIG. 1 to be published with the application.

- Enclosures:
- ☒ Specification (pages 1-9); claims (pages 10-13); abstract (page 14)
 - ☒ FIVE sheet(s) of FORMAL drawings
 - ☒ Declaration or Combined Declaration and Power of Attorney
 - ☒ Newly executed (original or copy)
 - ☒ Assignment with cover sheet
 - Assignee Name and Address: Samsung Electronics Co., Ltd.
 Suwon-city, Kyungki-do, Korea
 - ☒ Certified copy of priority document
 - ☒ Return Postcard

CLAIMS AS FILED

For	Number Filed	Number Extra	Rate	Basic Fee
Total Claims	27-20	7	x \$ 18 =	\$126
Independent Claims	3-3	0	x \$ 84 =	0
TOTAL FILING FEE				\$866



PTO Form 2038 authorizing credit card payment for the above-listed fees is enclosed.
Any deficiency or overpayment should be charged or credited to deposit account
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PATENT TRADEMARK OFFICE

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

Alan T. McCollom
Reg. No. 28,881

MARGER JOHNSON & McCOLLOM, P.C.
1030 SW Morrison Street
Portland, OR 97205
503-222-3613